

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



APPLICANT: Hamann et al.

EXAMINER: Patricia Ann George

SERIAL NO: 10/718,070

GROUP: Art Unit 1765

FILED: November 20, 2003

FOR: MULTI-BIT PHASE CHANGE MEMORY CELL AND MULTI-BIT PHASE CHANGE MEMORY INCLUDING THE SAME, METHOD OF FORMING A MULTI-BIT PHASE CHANGE MEMORY, AND METHOD OF PROGRAMMING A MULTI-BIT PHASE CHANGE MEMORY

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated August 24, 2005, applicant provisionally elects the claims of Group I (claim nos. 1-13 and 22-23), with traverse.

As set forth in M.P.E.P. § 803, if the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though the application contains claims to independent or distinct inventions.

While the multi-bit phase change memory cell and multi-bit phase change memory of Group I, the method of forming a multi-bit phase change memory cell of Group II and the method

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria VA 22313-1450 on September 20, 2005.

Dated:

Sept 20, 2005

Scott L. Appelbaum

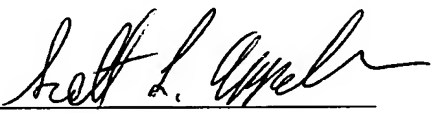
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of programming a multi-bit phase change memory cell of Group III may be distinct for the reasons set forth by the Examiner, it is respectfully submitted that simultaneous examination will not present an undue burden, much less any burden. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application.

An early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,


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